

**Course Schedule
UC4F1707EEE - TPM**

ASIA PACIFIC UNIVERSITY - APU

	2017												2018												2019																
	July	August	September	October	November	December	January	February	March	April	May	June	July	August	September	October	November	December	January	February	M																				
	1	2	3	0	1	2	2	0	1	1	2	0	0	1	2	2	0	1	1	2	0	1	1	2	0	1	1	2	0	1	1	2	0								
	7	4	1	7	4	1	8	4	1	8	5	2	9	6	3	0	6	3	0	7	4	1	8	5	1	8	5	2	9	6	3	0	7	4	1	7	4	1	8	5	4
Analogue Integrated Circuits And Systems (4)																																									
Analogue Integrated Circuits And Systems (4)																																									
Engineering Project Management (L)																																									
Group Design Project (L)																																									
Production Creation Technology (L)																																									
Project Phase I (L)																																									
VLSI Design (L)																																									
VLSI Design (LT)																																									
Engineer In Society (L)																																									
High Voltage Engineering (I)																																									
High Voltage Engineering (I)																																									
Project Phase II																																									

20 - July - 2017
 * This schedule may be subject to revision
 Legend:
 R - Revision Week
 E - Exam